



IFW
520.40847CC2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. ITOH, et al.
Application No.: 10/796,023
Filed: March 10, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH
IMPROVED STORAGE MOSFET ARRANGEMENT
Art Unit: 2818
Examiner: G. N. Aduong

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 29, 2004

Sir:

In response to the Office Action dated June 29, 2004, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims and

Remarks are included following the amendments.